

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Tetsuya HIRAOKA, et al.**

Serial No.: **Not Yet Assigned**

Filed: **October 10, 2001**

For: **SEMICONDUCTOR DEVICE WITH STACK OF SEMICONDUCTOR CHIPS**

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

October 10, 2001

Sir:

In compliance with 37 CFR 1.56, Applicants call to the attention of the Patent and Trademark Office the references listed on the attached PTO-1449.

Copies of the references are enclosed herewith.

In the event there are any fees due in connection with the filing of this paper, please charge Deposit Account No. 01-2340.

Respectfully submitted,
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Enclosures: PTO-1449; References (2)



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